

	<b>Document ID</b>	<b>Issue Date</b>	<b>Pages</b>	<b>Title</b>	<b>Current OR</b>
1	US 20060115033 A1	20060601	15	Data transmission circuit with serial interface and method for transmitting serial data	375/354
2	US 7123678 B2	20061017	10	RZ recovery	375/376
3	US 6304113 B1	20011016	9	Device for synchronizing a reference event of an analog signal on a clock	327/141
4	US 5487092 A	19960123	10	System for high-speed synchronization across clock domains	375/354
5	US 5272694 A	19931221	22	Synchronization of terminal stations in a multirate half-duplex tree-structured network	370/296
6	US 5258980 A	19931102	31	Radio channel switching control method	370/347
7	US 4965884 A	19901023	7	Data alignment method and apparatus	375/354
8	US 4635274 A	19870106	10	Bidirectional digital signal communication system	375/141

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<b>2</b>	Hendrickson; Norm
<b>3</b>	Dautriche; Pierre
<b>4</b>	Finney; Damon W. et al.
<b>5</b>	Bourgart; Fabrice et al.
<b>6</b>	Maebara; Akihiro et al.
<b>7</b>	Okura; David K. et al.
<b>8</b>	Kubota; Ichiro et al.

	<b>Document ID</b>	<b>Issue Date</b>	<b>Pages</b>	<b>Title</b>	<b>Current OR</b>
1	US 20060098771 A1	20060511	14	Method and apparatus for a digital-to-phase converter	375/371
2	US 6775342 B1	20040810	53	Digital phase shifter	375/371
3	US 6393083 B1	20020521	14	Apparatus and method for hardware implementation of a digital phase shifter	375/371
4	US 5511091 A	19960423	13	Clock synchronization control check system	375/226
5	US 5487092 A	19960123	10	System for high-speed synchronization across clock domains	375/354
6	US 5457719 A	19951010	12	All digital on-the-fly time delay calibrator	375/373
7	US 5050189 A	19910917	25	Multibit amplitude and phase modulation transceiver for LAN	375/223
8	US 5034967 A	19910723	23	Metastable-free digital synchronizer with low phase error	375/373

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<b>1</b>	Cafaro; Nicholas G. et al.
<b>2</b>	Young; Steven P. et al.
<b>3</b>	Beukema; Troy J.
<b>4</b>	Saito; Naritoshi
<b>5</b>	Finney; Damon W. et al.
<b>6</b>	Guo; Bin et al.
<b>7</b>	Cox; William M. et al.
<b>8</b>	Cox; William M. et al.